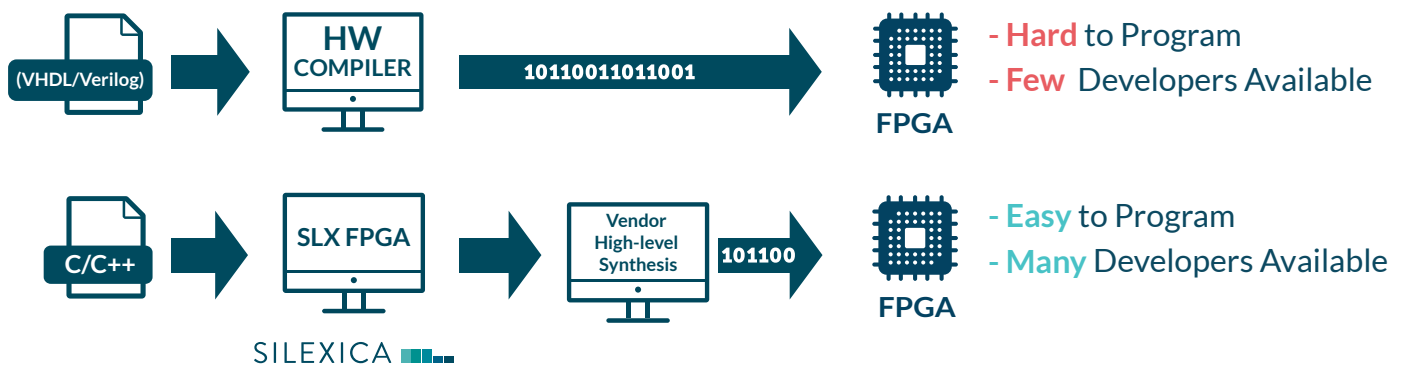


Accelerate the journey from C/C++ to Hardware

SLX FPGA helps convert applications or algorithms written in C/C++ into an optimized hardware design for implementation in an FPGA. Combining the ease of programming a standard high-level programming language, such as C/C++ with the performance of a dedicated FPGA hardware implementation, **SLX FPGA** provides fast time to market and a performance-optimized solution.



Accelerate design cycles

SLX eliminates the need for specialized programming skills required to program FPGAs and can reduce design cycles by up to 100X by designing at a higher level of abstraction and removing performance roadblocks in C/C++ code. By significantly reducing design cycles, **SLX FPGA** enables customers to beat the competition by getting their products to market faster.

Accelerate performance

SLX FPGA also automatically finds functions in application code which can be optimized for implementation in an FPGA and performs system level optimizations which can increase performance by up to 1000x, allowing customers to differentiate their products by achieving new levels of performance that would not be possible using standard processors.

Accelerate your success!

SLX FPGA enables you to beat the competition to market and differentiate your products by enabling the benefits of hardware acceleration with the ease of use of a standard high-level programming language. **Contact us to take your products to the next level!**